REMARKS

Claims 24-25 and 27-34 are presented for examination. Claims 26 and 35 are canceled. Claims 24, 30, and 34 are amended. Applicants respectfully request reconsideration of these claims as presently presented. Generally, the claims have been amended to incorporate subject matter from the dependent claims into the independent claims. The amendments further define specific claimed elements such as the lighting logic unit and the multiplication logic unit. Although *Krech* may disclose a multiplier and a lighting detector as set forth in the Office Action, *Krech* does not teach or disclose the specific components and arrangements as set forth in the claims. Some of these distinctions are described below.

Independent claim 24

Independent claim 24 is amended herein to incorporate the subject matter of claim 26. For example, claim 24 now includes the following limitation:

> wherein the lighting logic unit is coupled to the multiplication logic unit via a conversion module adapted for converting scalar vertex data to vector vertex data.

The office action points to figure 5 as support for rejecting this subject matter. But figure 5 does not teach or suggest the use of a conversion module as recited in applicants' claim. *Krech's* figure 5, for example, illustrates a stack of processing elements (51) that includes a multiplier. This figure does not illustrate any conversion module adapted to convert scalar vertex data to vector vertex data. Similarly, the text supporting figure 5 does not describe any conversion module for converting scalar vertex data to vector vertex data. Accordingly, *Krech* cannot teach, suggest or render obvious claim 24.

Independent claims 30 and 34

Independent claims 30 and 34 are amended herein to incorporate the subject matter of claim 25. For example, claim 30 now includes the following limitation:

> wherein the multiplication logic unit has a feedback loop coupled to an input of the multiplication logic unit.

This feedback loop is an architectural limitation that is not taught or suggested by *Krech*. Although *Krech* may teach the vertex looping routine described in the office action, this vertex looping routine is not equivalent to the architectural feedback loop limitation of claim 25.

The architecture of the *Krech* device is illustrated in figures 3-5, and the multiplication unit is labeled as reference number 55. None of these figures show any type of feedback loop coupled to the input of the multiplication unit 55. And nothing in the written description discloses a feedback loop coupled to the input of the multiplication unit 55. Accordingly, *Krech* cannot teach, suggest or render obvious claims 30 and 34, both of , which require a feedback.

CONCLUSION

Applicants respectfully submit that the claims as presented are distinguishable over the applied references. Because no other objections or rejections are outstanding, Applicants request an indication of allowability.

The Commissioner is hereby authorized to charge any appropriate fees under 37 C.F.R. §§1.16, 1.17, and 1.21 that may be required by this paper, and to credit any overpayment, to Deposit Account No. 03-3117.

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Respectfully submitted,

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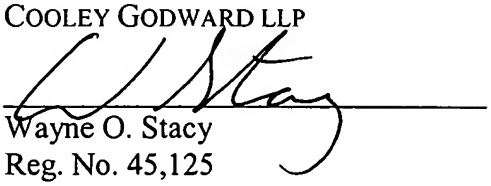
By: 

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